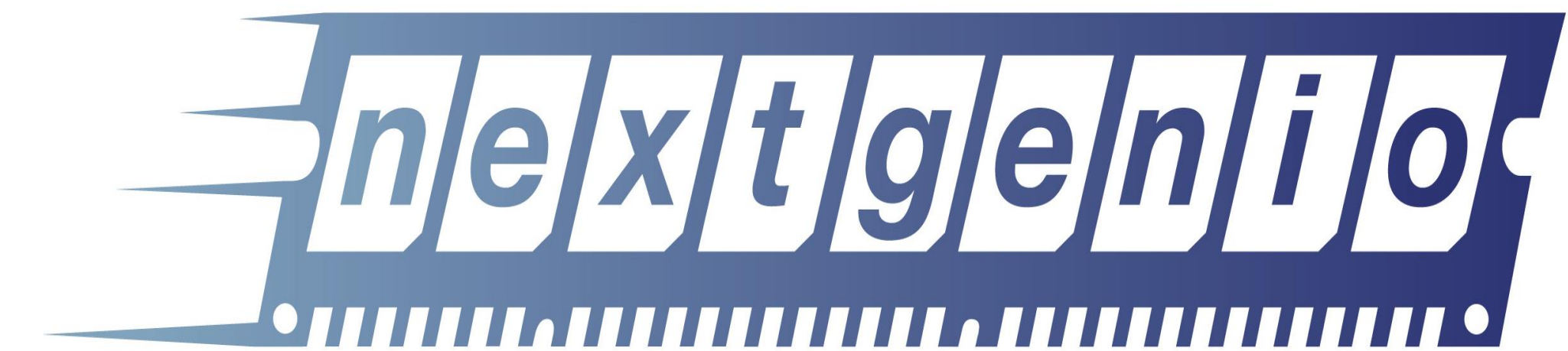


NEXTGenIO Project

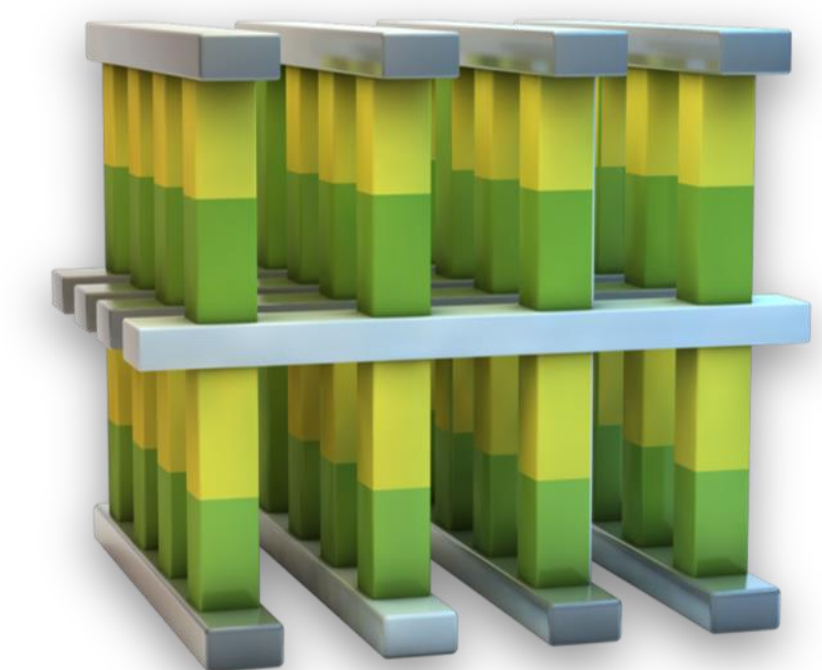


NEXTGenIO addresses two important HPC challenges

- Growing gap between compute and I/O performance
- New classes of workloads that require large volumes of direct-access memory

NEXTGenIO proposes and implements a novel system architecture

- Intel® 3D XPoint™ technology bridging gap between memory and storage
- Architecture providing scalable compute and I/O
- Prototype system becoming available Q4/2017



NEXTGenIO emphasizes a co-design approach

- Real-live usage scenarios and key workloads shape I/O and memory requirements
- Flexible systemware ensures scalability and efficient resource use
- Leading SW development tools facilitate application evolution
- Thorough architecture and system evaluation guiding development

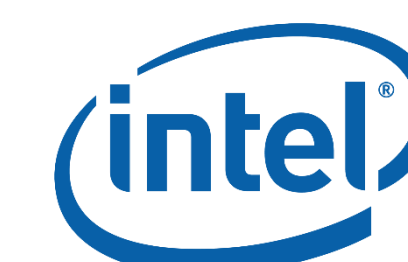


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